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## » Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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 Septi, M.; Bedda, M.;  
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 Chunqiang Tang; Deqing Chen; Dwarkadas, S.; Scott, M.L.;  
Distributed Computing Systems, 2003. Proceedings. 23rd International Confer  
 19-22 May 2003 Page(s):560 - 569  
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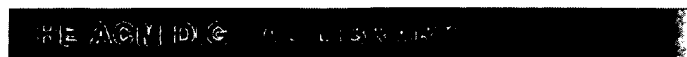
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### 1 [SoftFLASH: analyzing the performance of clustered distributed virtual shared memory](#)



Andrew Erlichson, Neal Nuckolls, Greg Chesson, John Hennessy  
 September 1996 **ACM SIGPLAN Notices , ACM SIGOPS Operating Systems Review , Proceedings of the seventh international conference on Architectural support for programming languages and operating systems ASPLOS-VII**, Volume 31 , 30 Issue 9 , 5

Publisher: ACM Press

 Full text available: [pdf\(1.29 MB\)](#)

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One potentially attractive way to build large-scale shared-memory machines is to use small-scale to medium-scale shared-memory machines as clusters that are interconnected with an off-the-shelf network. To create a shared-memory programming environment across the clusters, it is possible to use a virtual shared-memory software layer. Because of the low latency and high bandwidth of the interconnect available within each cluster, there are clear advantages in making the clusters as large as possible ...

### 2 [Multi-core design II: Hardware support for spin management in overcommitted virtual machines](#)



Philip M. Wells, Koushik Chakraborty, Gurindar S. Sohi  
 September 2006 **Proceedings of the 15th international conference on Parallel architectures and compilation techniques PACT '06**

Publisher: ACM Press

 Full text available: [pdf\(261.29 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Multiprocessor operating systems (OSs) pose several unique and conflicting challenges to System Virtual Machines (System VMs). For example, most existing system VMs resort to gang scheduling a guest OS's virtual processors (VCPUs) to avoid OS synchronization overhead. However, gang scheduling is infeasible for some application domains, and is inflexible in other domains. In an overcommitted environment, an individual guest OS has more VCPUs than available physical processors (PCPUs), precluding t ...

**Keywords:** chip multiprocessors, synchronization overhead, virtual machines

### 3 [Q focus: system performance: Modern Performance Monitoring](#)



Mark Purdy



cross-call interrupt

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KB Normoyle, Z Ebrahim, S Nishtala, WC Van Loo, CE ... - US Patent 5,892,957, 1999 - Google Patents

... [45] Date of Patent: [54] METHOD AND APPARATUS FOR **INTERRUPT** COMMUNICATION IN

PACKET-SWITCHED MICROPROCESSOR- BASED COMPUTER SYSTEM ... and issues **interrupt** 1500 ...

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T Koike, H Sugiura - US Patent 4,494,215, 1985 - Google Patents

... status information; two disk control adapters with a **cross-call** function, connected ... means and said decoder means, for generating an **interrupt** request signal. ...

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P Levels, P Hyperprivileged - opensparc.sunsource.net

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Hyperprivileged **Interrupt** Pending Register (HINTP) . . . . .

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SoftFLASH: Analyzing the Performance of Clustered Distributed Virtual Shared Memory

SG Inc - cs.wm.edu

... To synchronize the TLBs on a multiprocessor, the kernel must perform a **cross call** to **interrupt** all the processors that might contain the mapping. ...

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B Falsafi, DA Wood - 1996 - cs.wisc.edu

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S Koyama - US Patent 5,088,029, 1992 - Google Patents

... 20 from an input/output channel of a data processing 1er so as to realize a **cross call** function. ... The **cross call** function operates i the above construc- ...

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B Falsafi, DA Wood - Proceedings of the Third IEEE Symposium on High-Performance ..., 1997 - doi.ieeecs.org

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